

FIG. 2

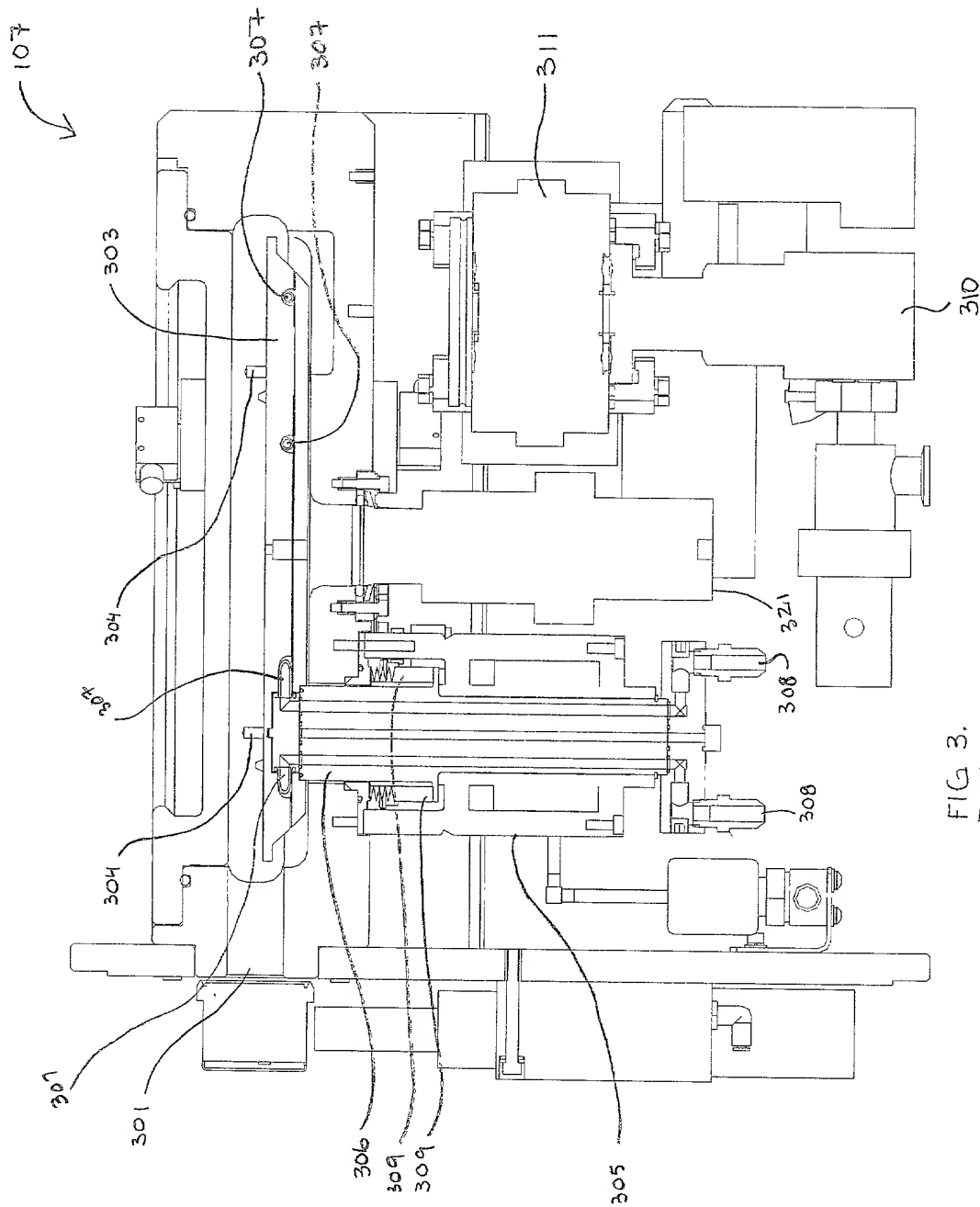


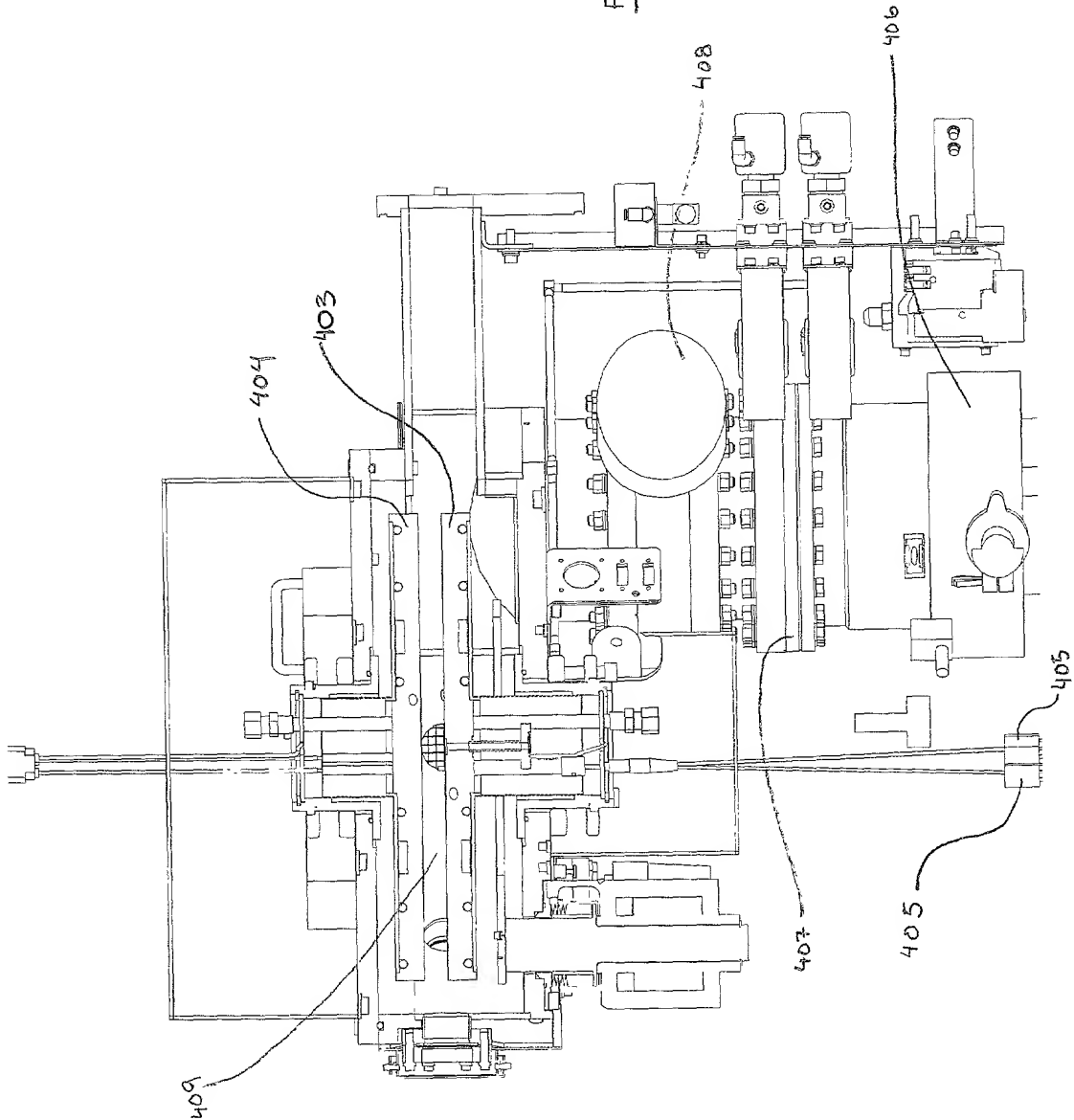
FIG. 3.

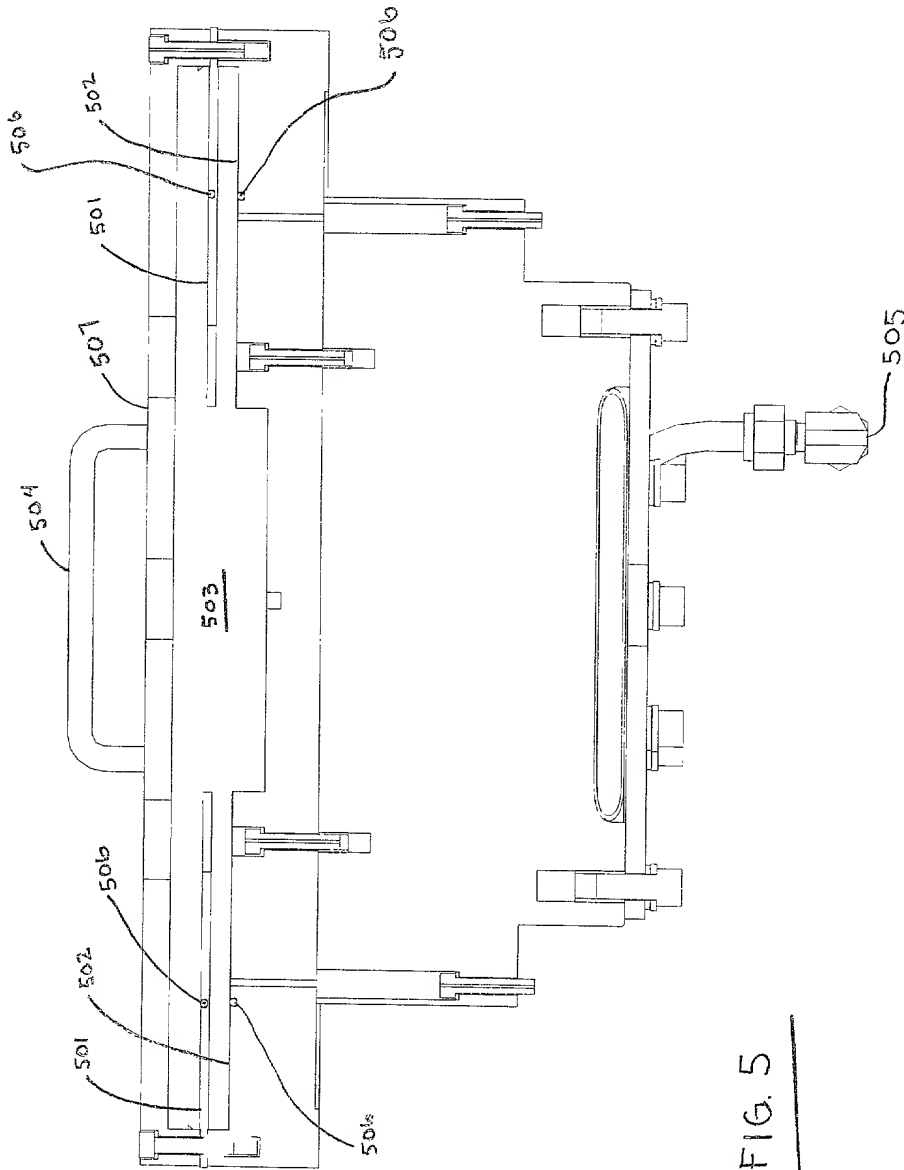
Architecture For High Throughput Semiconductor  
Processing Applications

Inventors: Craig L. Stevens, et al.

Serial #: 09/872,796

FIG. 4





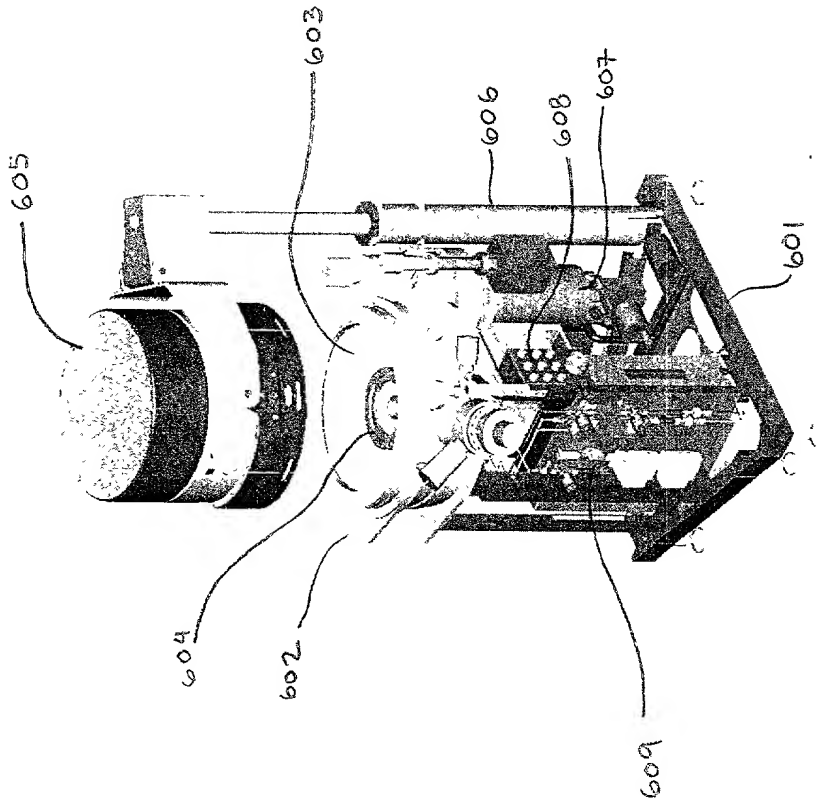


FIG. 6

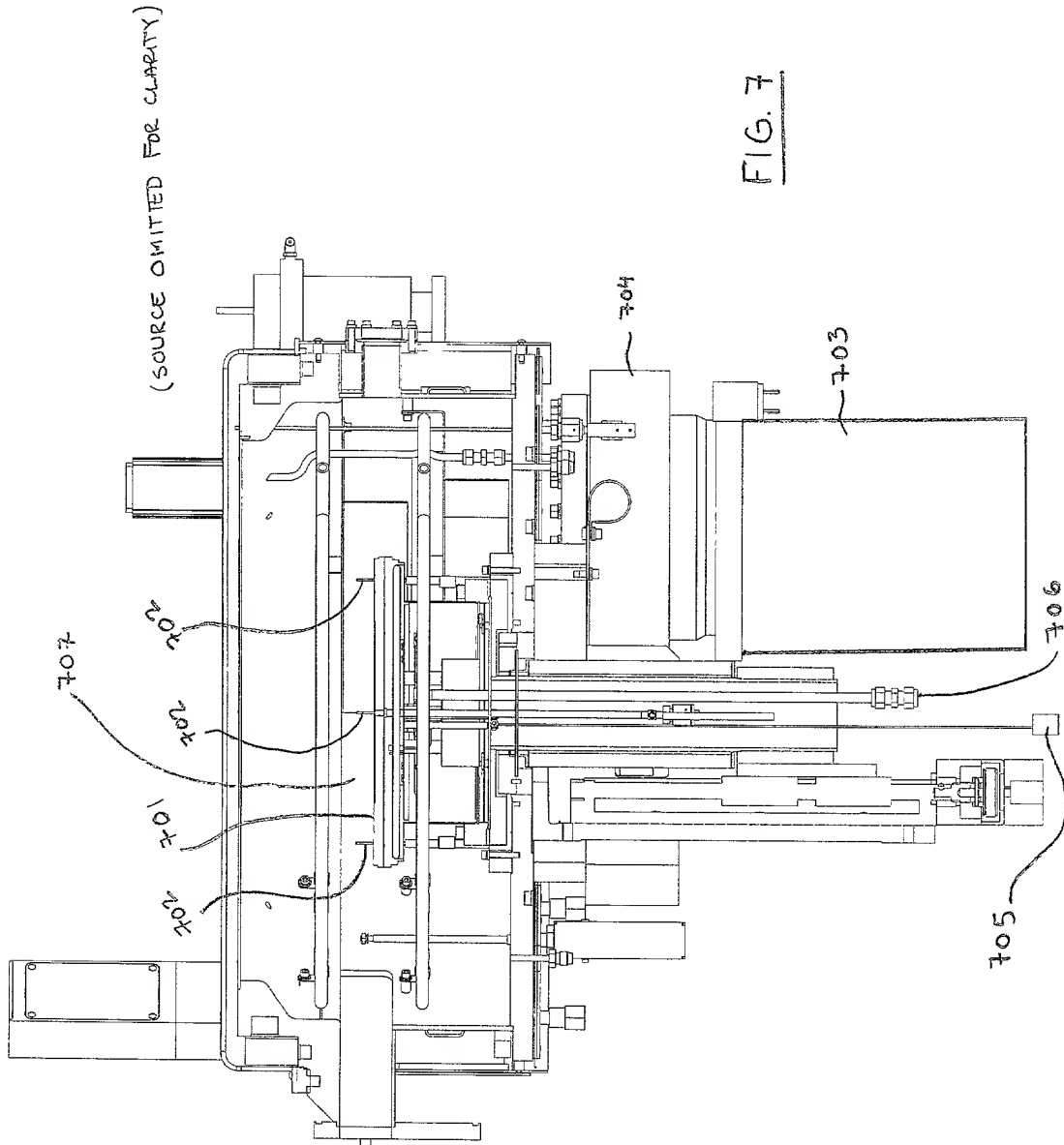
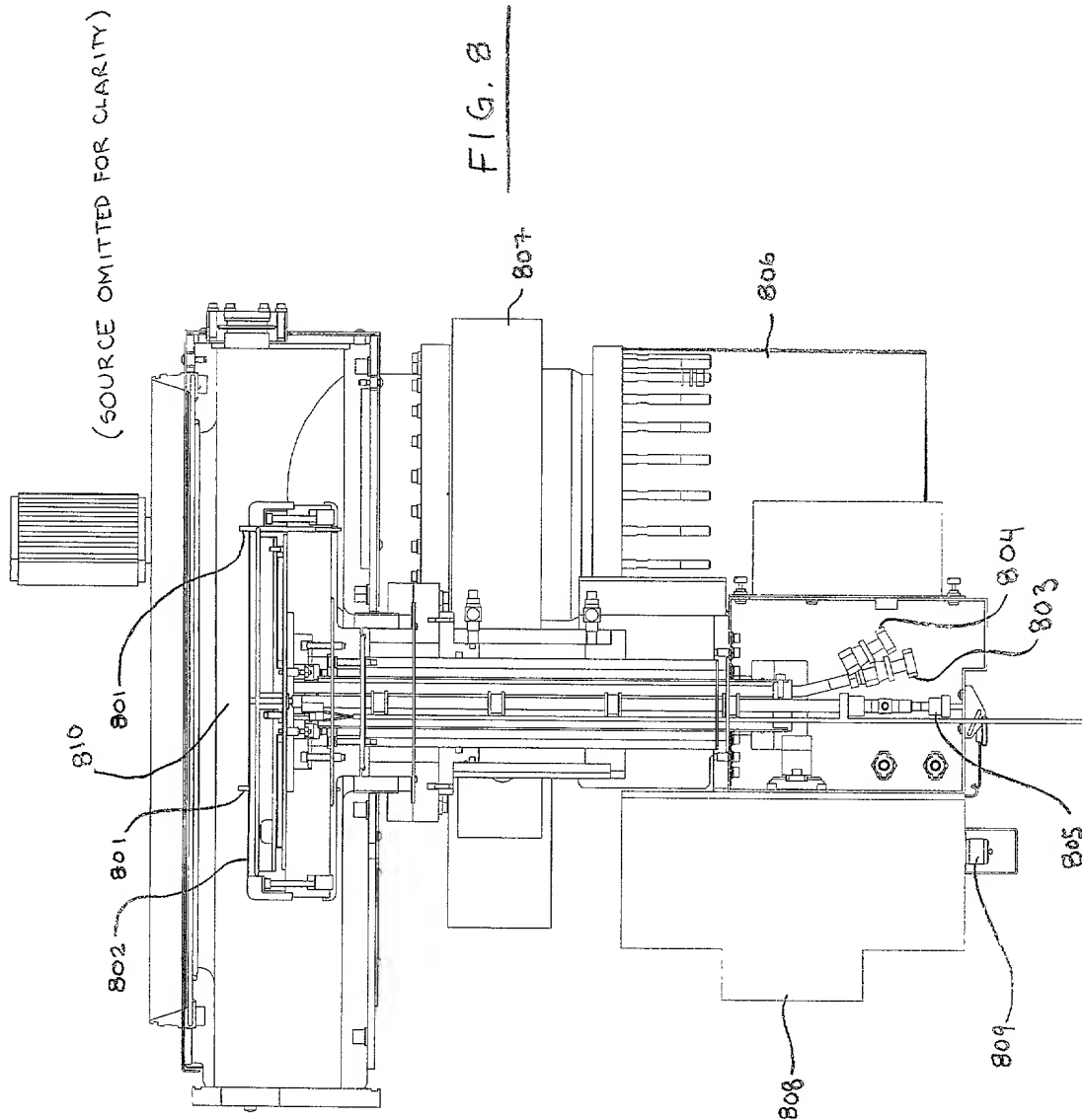


FIG. 7







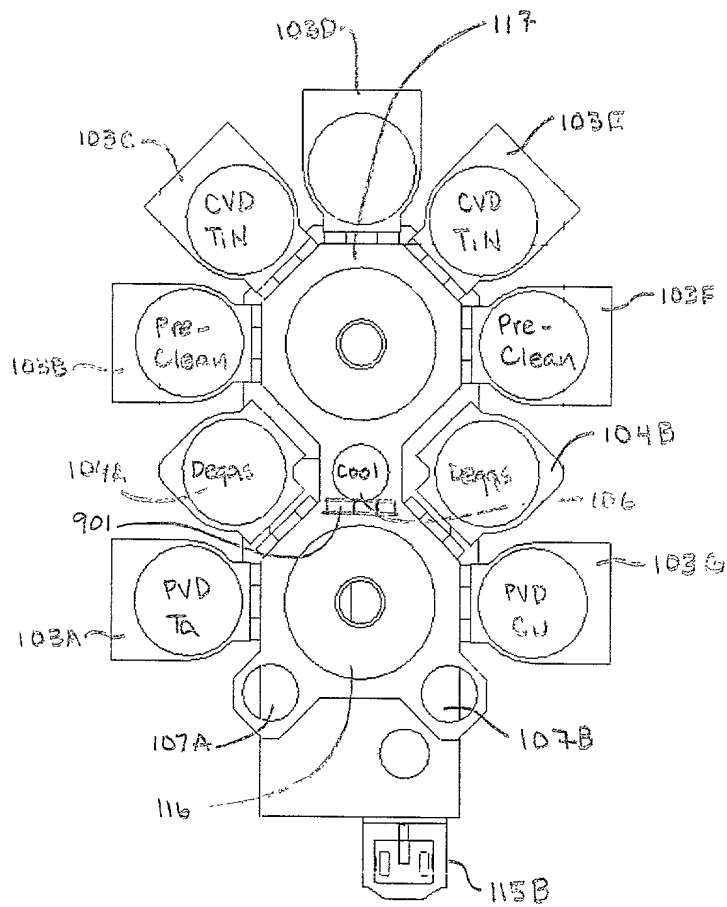


FIG. 9B



•



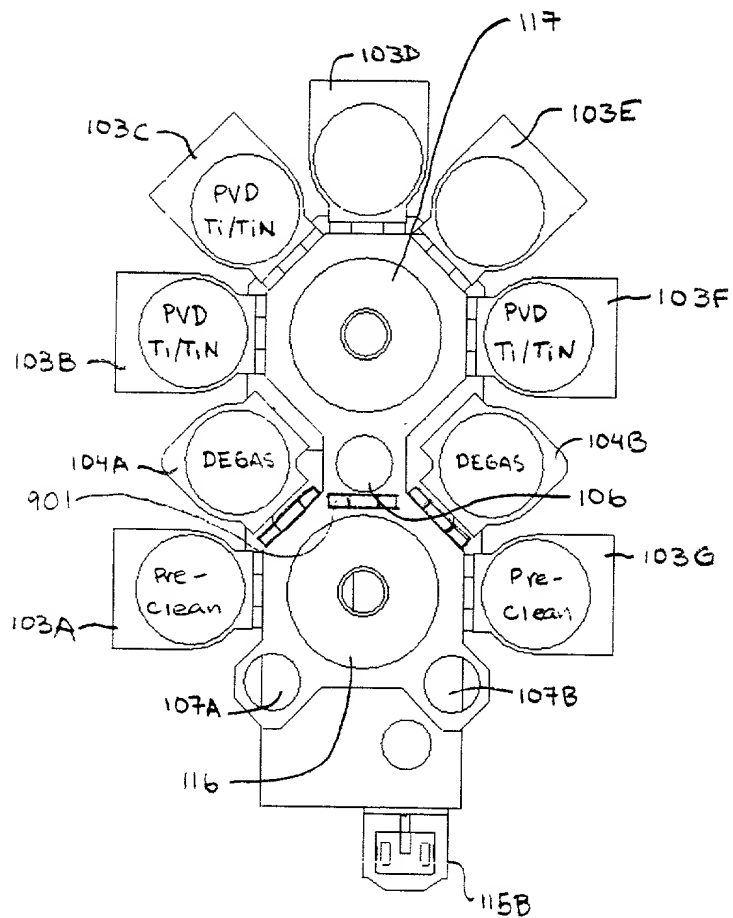


FIG. 10A

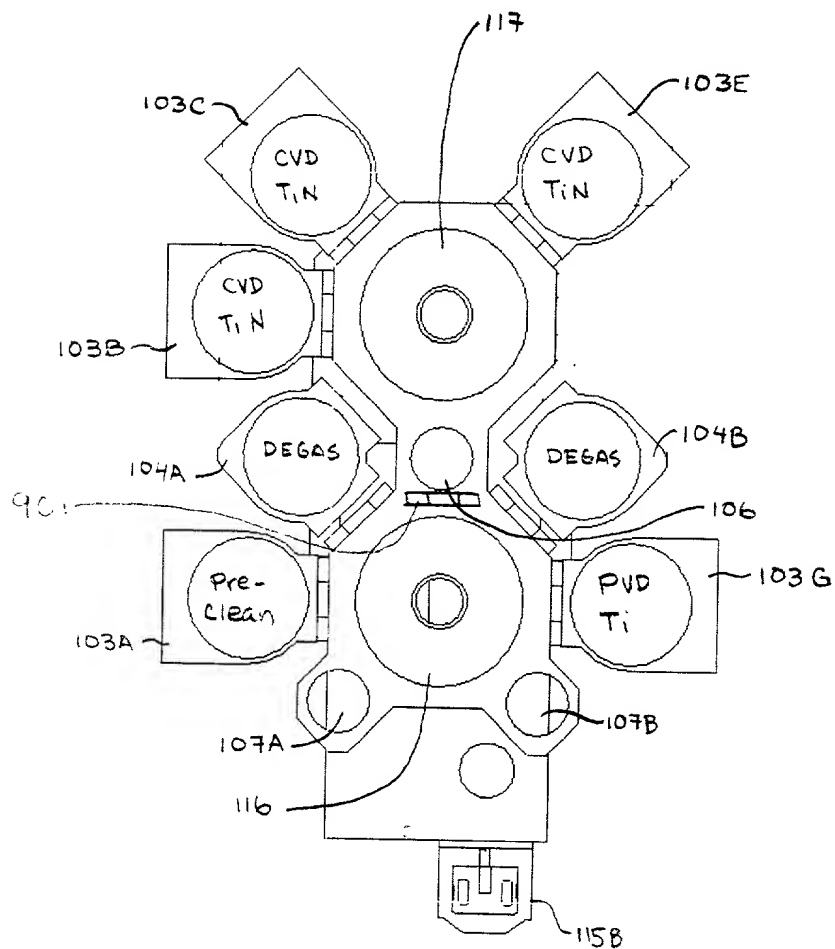


FIG. 10B